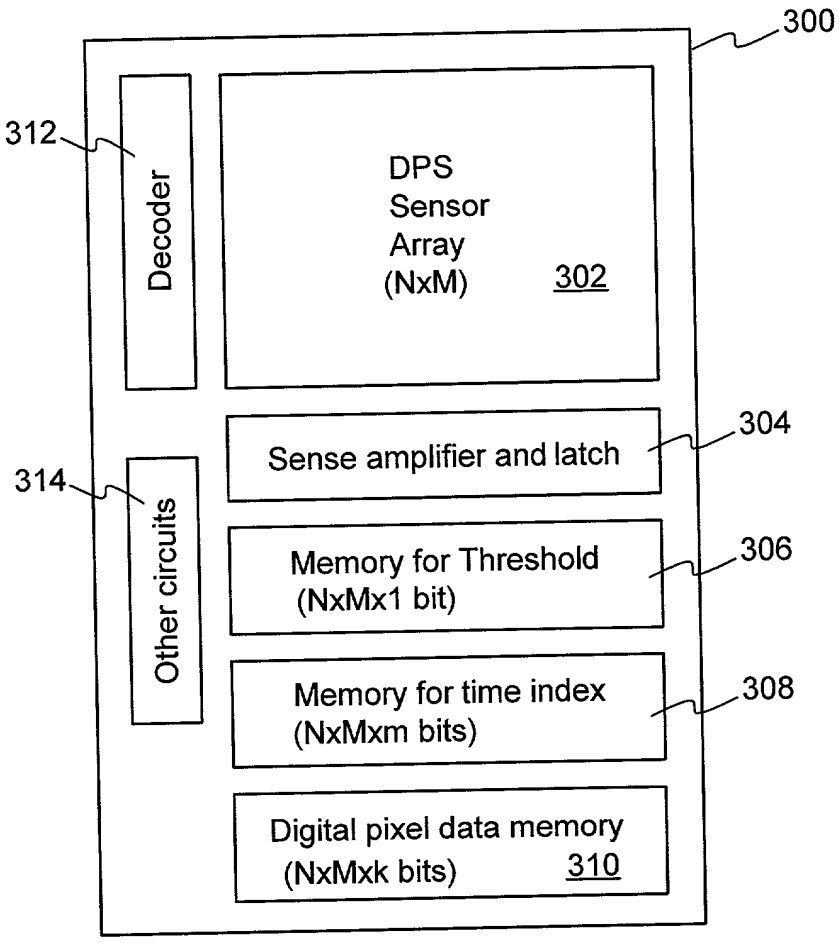
300
312
Decoder
DPS
Sensor
Array
(NxM)
302
304
Sense amplifier and latch
314
Other circuits
Memory for Threshold
(NxMx1 bit)
306
Memory for time index
(NxMxm bits)
308
Digital pixel data memory
(NxMxk bits)
310

Figure 1

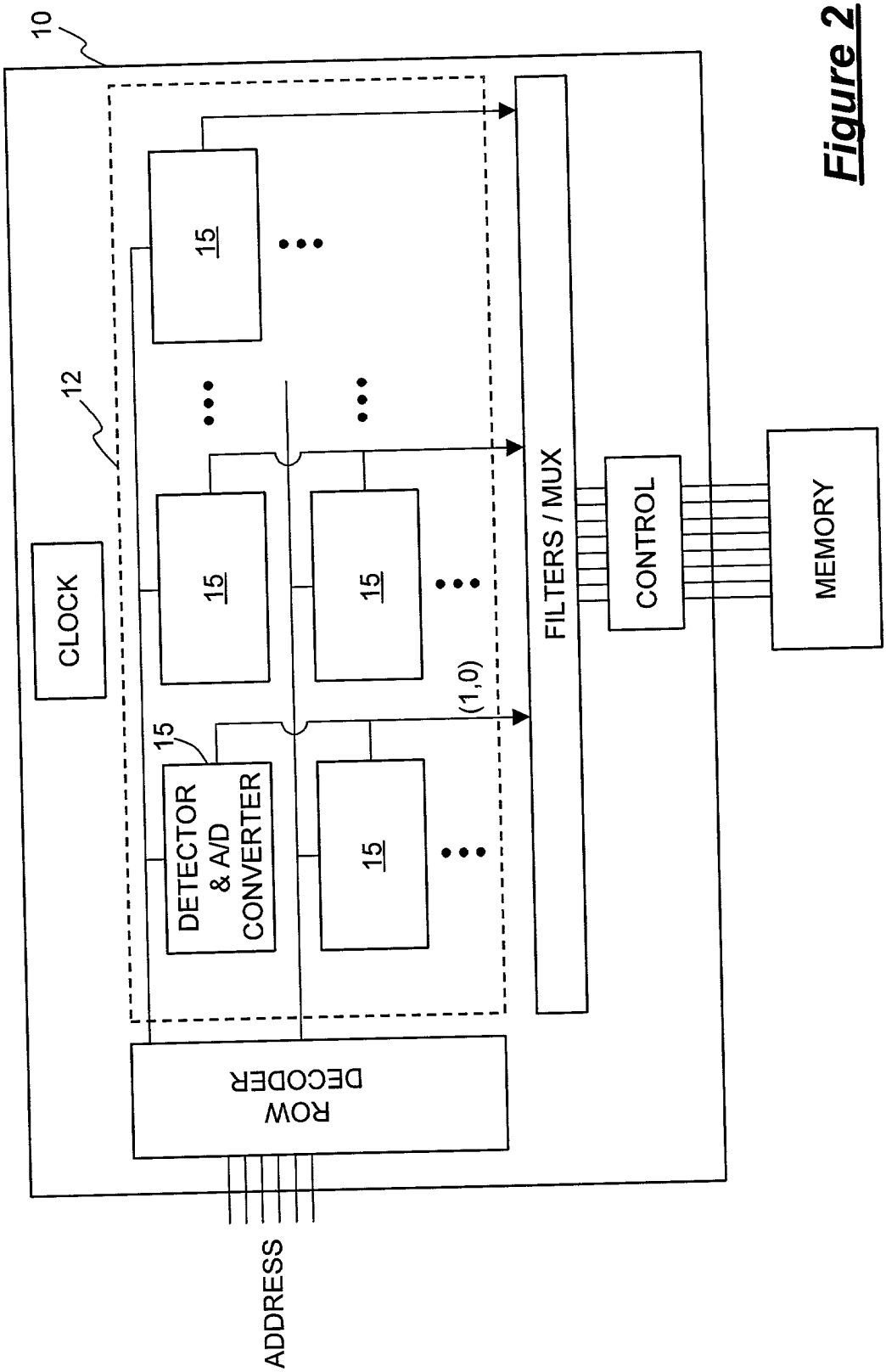
10
12
CLOCK
15
DETECTOR & A/D CONVERTER
(1,0)
ROW DECODER
ADDRESS
FILTERS / MUX
CONTROL
MEMORY

Figure 2

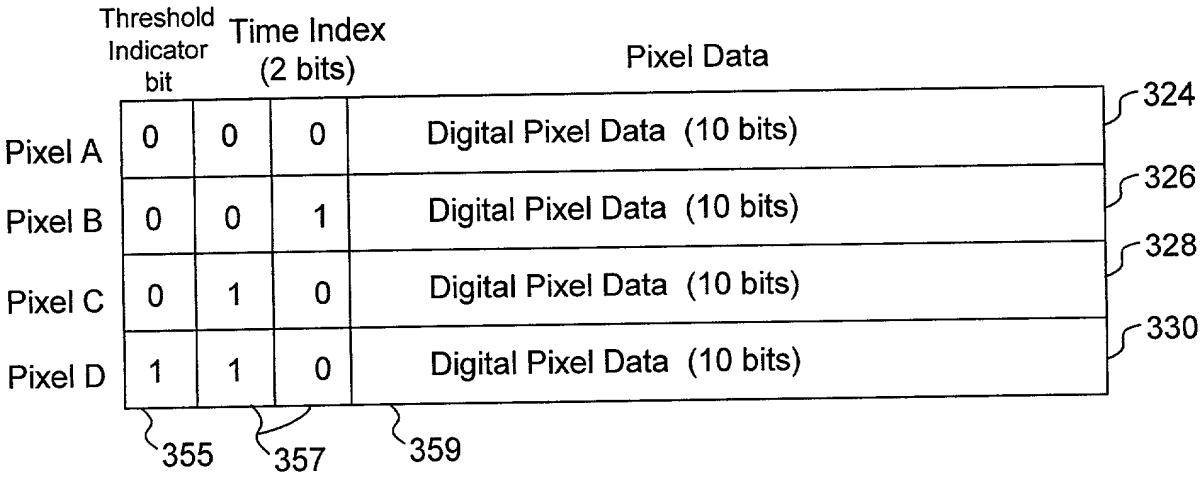

Figure 3

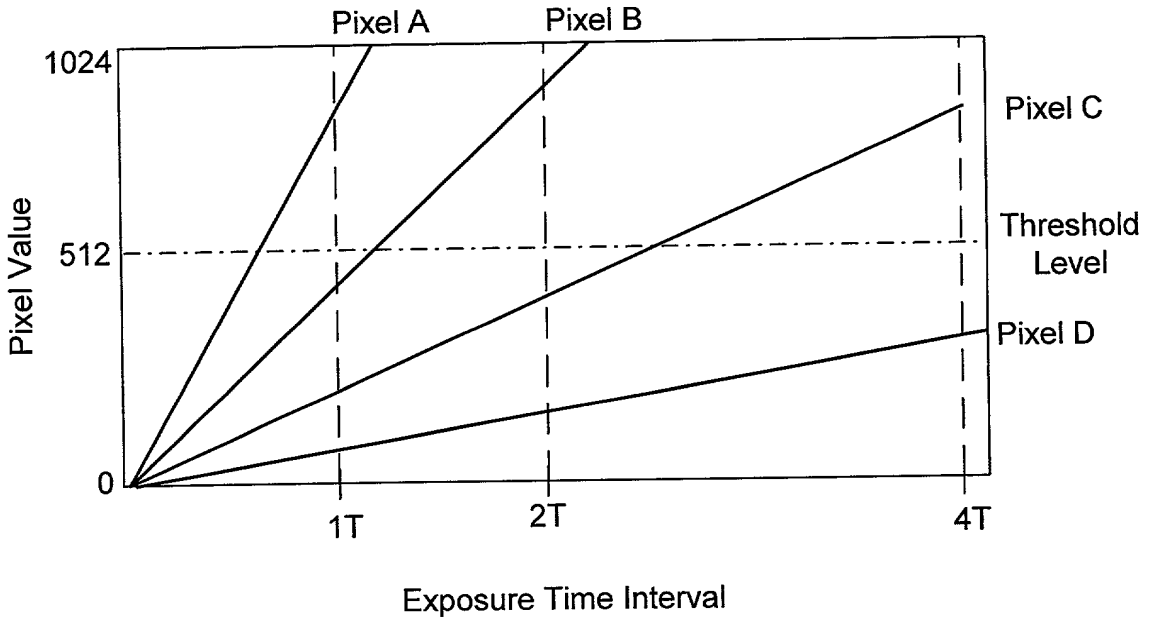

Figure 4

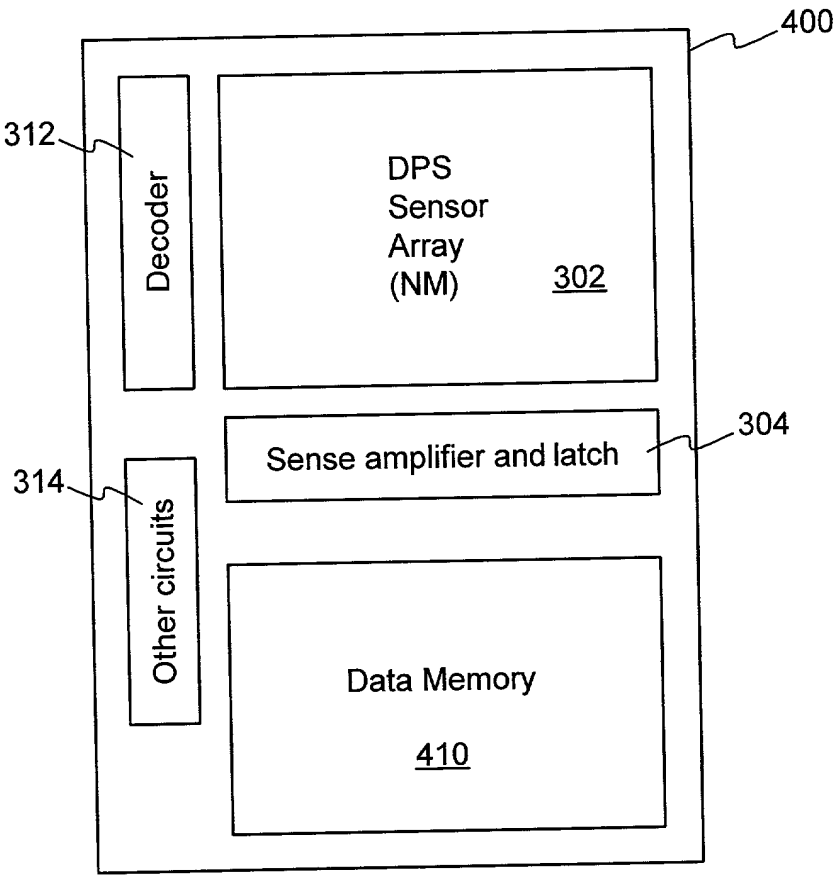
400
312
Decoder
DPS
Sensor
Array
(NM)
302
304
Sense amplifier and latch
314
Other circuits
Data Memory
410

Figure 5

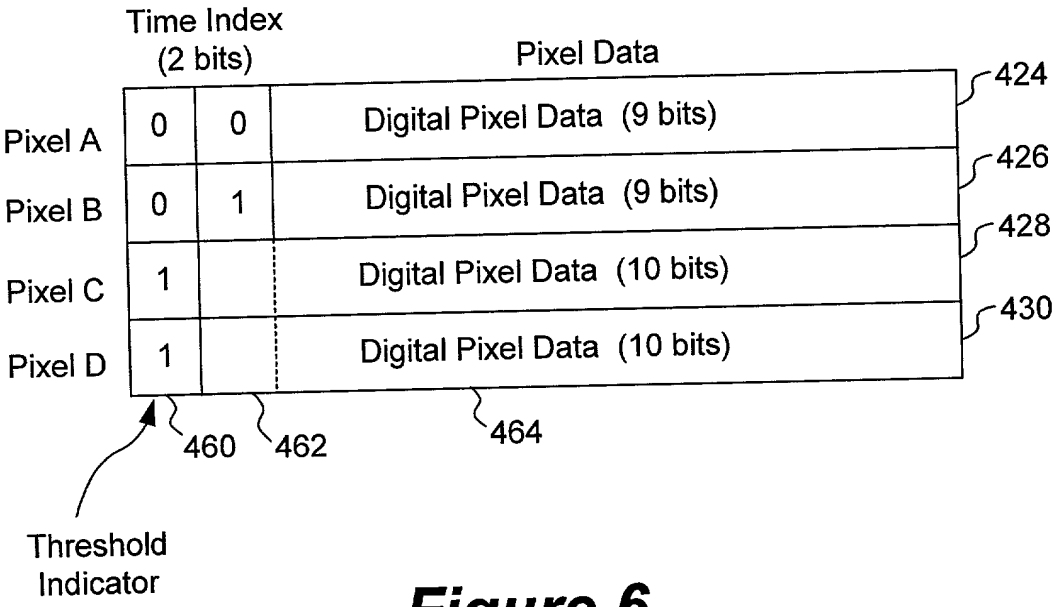

Figure 6

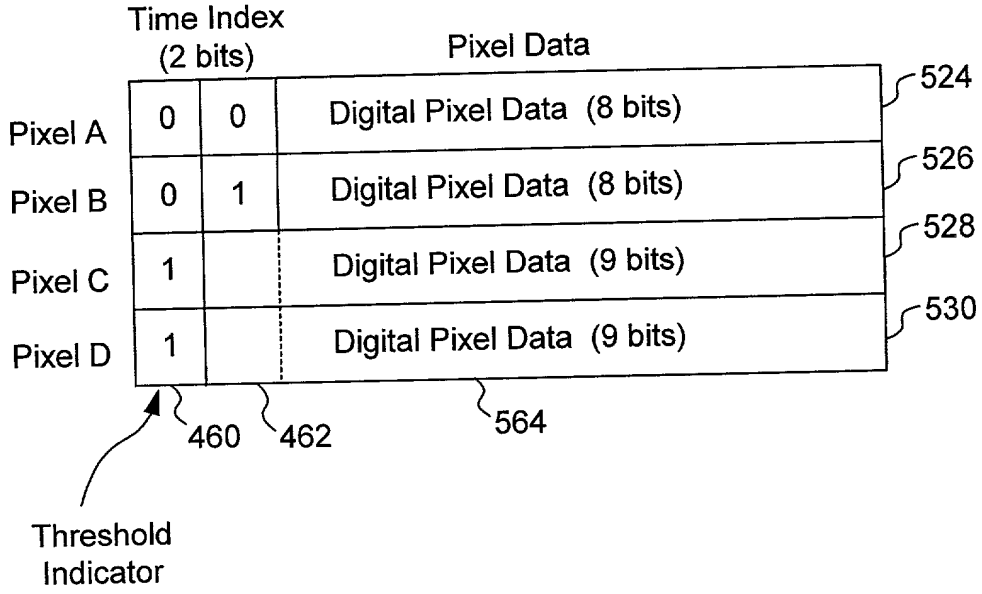

Figure 7

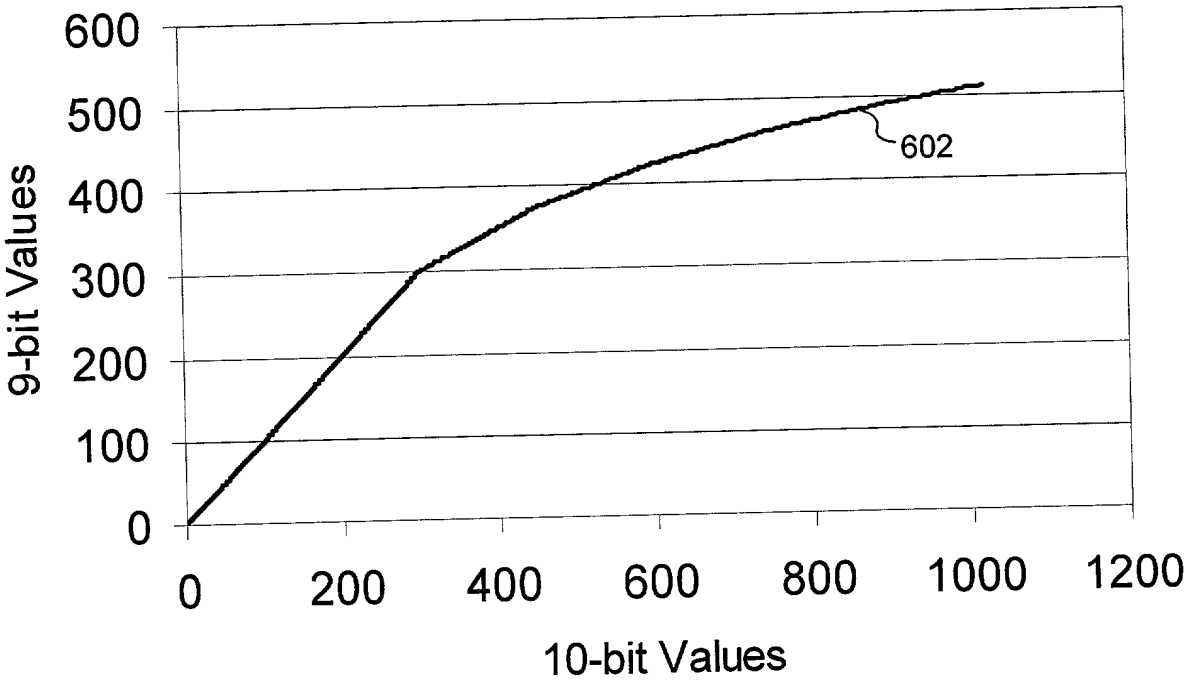
600
500
400
300
200
100
0
9-bit Values
602
0
200
400
600
800
1000
1200
10-bit Values

Figure 8

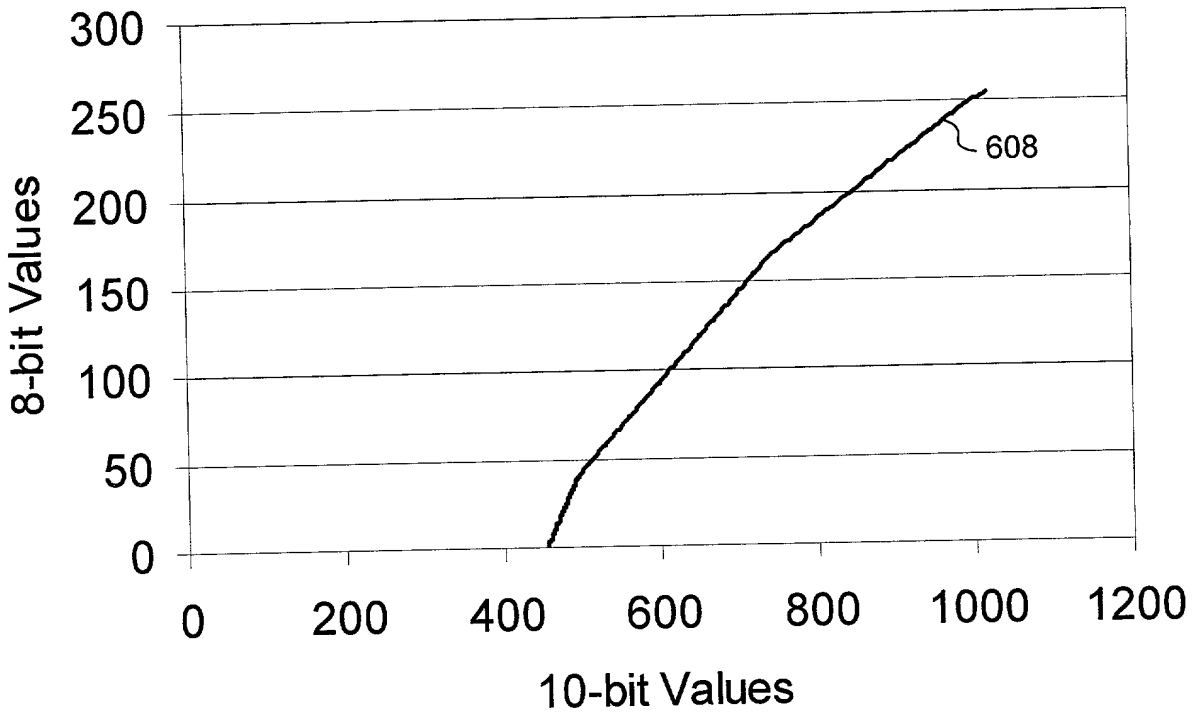
300
250
200
150
100
50
0
8-bit Values
608
0
200
400
600
800
1000
1200
10-bit Values

Figure 9